New Jersey Semi-Conductor Products, Inc.

20 STERN AVE. SPRINGFIELD, NEW JERSEY 07081 U.S.A.

TELEPHONE: (973) 376-2922 (212) 227-6005 FAX: (973) 376-8960

UHF power transistor

BLW90

DESCRIPTION

N-P-N silicon planar epitaxial transistor suitable for transmitting applications in class-A, B or C in the u.h.f. and v.h.f. range for a nominal supply voltage of 28 V. The transistor is resistance stabilized and is guaranteed to withstand infinite VSWR at rated output power. High reliability is ensured by a **gold sandwich metallization.** The transistor is housed in a $\frac{1}{4}$ " capstan envelope with a ceramic cap. All leads are isolated from the stud.

QUICK REFERENCE DATA

R.F. performance up to Th = 25 °C in an unneutralized common-emitter class-B circuit

MODE OF OPERATION	V _{CE}	f	PL	G _p	ון
	V	MHz	W	dB	%
C.W.	28	470	4	> 11	> 55

PIN CONFIGURATION



PINNING - SOT122A.

PIN	DESCRIPTION
1	collector
2	emitter
3	base
4	emitter



NJ Semi-Conductors reserves the right to change test conditions, parameters limits and package dimensions without notice information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

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UHF power transistor

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Collector-emitter voltage				
(peak value); V _{BE} = 0	V _{CESM}	max.	60	V
open base	V _{CEO}	max.	30	V
Emitter-base voltage (open collector)	V _{EBO}	max.	4	v
Collector current				
d.c. or average	IC; IC(AV)	max.	0,62	А
(peak value); f > 1 MHz	I _{CM}	max.	2,0	А
Total power dissipation (d.c. and r.f.) up to T_{mb} = 25 $^{\circ}C$	P _{tot}	max.	18,6	w
Storage temperature	T _{sta}	65 to -	+ 150	°C
Operating junction temperature	T _i	max.	200	°C





THERMAL RESISTANCE

(dissipation = 6 W; T_{mb} = 73,6 °C, i.e. T_h = 70 °C)

From junction to mounting base			
(d.c. and r.f. dissipation)	R _{th j-mb}	=	9,0 K/W
From mounting base to heatsink	R _{th mb-h}	=	0,6 K/W
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CHARACTERISTICS					
T _j = 25 [™] C					
Collector-emitter breakdown voltage					
$V_{BE} = 0; I_{C} = 4 \text{ mA}$	V _{(BR)CES}	>	60	V	
Collector-emitter breakdown voltage					
open base; I_C = 20 mA	V _{(BR)CEO}	>	30	V	
Emitter-base breakdown voltage					
open collector; I _E = 2 mA	V _{(BR)EBO}	>	4	V	
Collector cut-off current					
V _{BE} = 0; V _{CE} = 30 V	I _{CES}	<	2	mA	
Second breakdown energy; L = 25 mH; f = 50 Hz					
open base	E _{SBO}	>	1	mJ	
R_{BE} = 10 Ω	E _{SBR}	>	1	mJ	
D.C. current gain ⁽¹⁾	h	typ.	40		
$I_{\rm C}$ = 0,3 A; $V_{\rm CE}$ = 5 V	UFE	10 to 100			
Collector-emitter saturation voltage ⁽¹⁾					
I _C = 1,0 A; I _B = 0,2 A	V _{CEsat}	typ.	0,9	V	
Transition frequency at f = 500 MHz $^{(1)}$					
-I _E = 0,3 A; V _{CB} = 28 V	f⊤	typ.	1,2	GHz	
–I _E = 1,0 A; V _{CB} = 28 V	f _T	typ.	0,9	GHz	
Collector capacitance at f = 1 MHz					
$I_{E} = I_{e} = 0; V_{CB} = 28 V$	C _c	typ.	8,4	рF	
Feedback capacitance at f = 1 MHz					
$I_{\rm C}$ = 20 mA; $V_{\rm CE}$ = 28 V	C _{re}	typ.	3,6	рF	
Collector-stud capacitance	C_{cs}	typ.	1,2	pF	

Note

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1. Measured under pulse conditions: $t_p \le 200 \ \mu s; \ \delta \le 0,02.$

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PACKAGE OUTLINE

Studded ceramic package; 4 leads



SOT122A