

Silicon NPN Power Transistors

BUW34

DESCRIPTION

With TO-3 package
 High breakdown voltage

APPLICATIONS

For high voltage fast switching applications

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

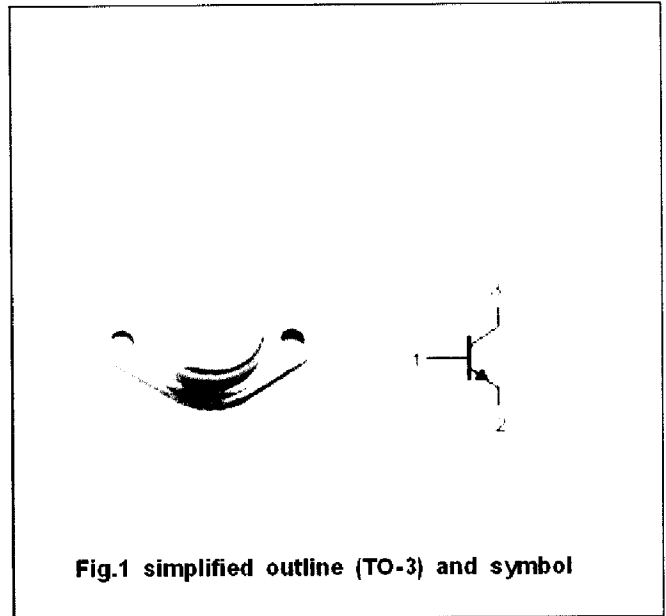


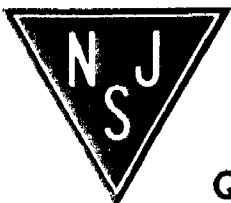
Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CB0}	Collector-base voltage	Open emitter	500	V
V _{CE0}	Collector-emitter voltage	Open base	400	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		10	A
I _{CM}	Collector current-peak		15	A
I _B	Base current		5	A
P _T	Total power dissipation	T _C 25	125	W
T _J	Junction temperature		200	
T _{stg}	Storage temperature		-65~200	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th J-C}	Thermal resistance from junction to case	1.4	/W



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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE0(BUS)}	Collector-emitter sustaining voltage	I _C =100mA ; I _B =0	400			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =5A ; I _B =1A			1.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =5A ; I _B =1A			1.5	V
I _{CEs}	Collector cut-off current	V _{CE} =500V ; V _{BE} =0 T _C =125			0.1 3.0	mA
I _{EB0}	Emitter cut-off current	V _{EB} =7V ; I _C =0			1	mA
h _{FE1}	DC current gain	I _C =1A ; V _{CE} =5V	15		50	
h _{FE2}	DC current gain	I _C =5A ; V _{CE} =5V	8			

Switching times

t _{on}	Turn-on time	I _C =5A ; I _{B1} =- I _{B2} =1A V _{CC} =250V			0.7	s
t _s	Storage time				3.0	s
t _f	Fall time				0.8	s

PACKAGE OUTLINE

