20 STERN AVE. SPRINGFIELD, NEW JERSEY 07081 U.S.A. TELEPHONE: (201) 376-2922 (212) 227-6005 FAX: (201) 376-8960

## Silicon Controlled Rectifier Reverse Blocking Triode Thyristor

... designed for industrial and consumer applications such as power supplies; battery chargers; temperature, motor, light, and welder controls.

• Economical for a Wide Range of Uses

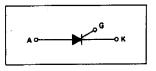
MAXIMUM RATINGS

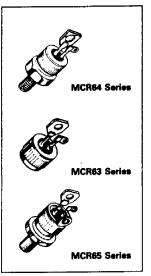
- High Surge Current ITSM = 550 Amps
- Rugged Construction in Either Pressfit, Stud, or Isolated Stud
- Glass Passivated Junctions for Maximum Reliability

#### Value Unit Rating Symbol VDRM<sup>(1)</sup> Volts Peak Repetitive Forward and Reverse 25 **Blocking Voltage** -1 or 50 -2 VRRM 100 -3 200 MCR63 -4 300 MCR64 -5 MCR65 -6 400 -7 500 600 -8 700 -9 800 -10 Volts VRSM Non-Repetitive Peak Reverse Blocking Voltage 35 (t ≤ 5 ms) -1 75 -2 150 -3 -4 300 MCR63 -5 400 MCR64 MCR65 -6 500 -7 600 700 -8 800 -9 -10 900 55 Amps Forward Current RMS IT(RMS) 550 Amps Peak Surge Current <sup>I</sup>TSM (One cycle, 60 Hz, $T_J = -40$ to $+125^{\circ}$ C) A<sup>2</sup>s |2t 1255 **Circuit Fusing Considerations** $(T_J = -40 \text{ to } + 125^{\circ}\text{C}, t = 1 \text{ to } 8.3 \text{ ms})$ Watts PGFM 20 Peak Gate Power 0.5 Watt PGF(AV) Average Gate Power (Pulse Width $\leq 2 \mu s$ ) 2 Amps Peak Forward Gate Current GFM Volts 10 Peak Gate Voltage — Forward VGFM 10 VGRM Reverse °C -40 to +125 Тj **Operating Junction Temperature Range** °C -40 to +150 Tstg Storage Temperature Range in. łb. 30 Stud Torque

# MCR63-1 thru 10 MCR64-1 thru 10 MCR65-1 thru 10

SCRs 55 AMPERES RMS 25 thru 800 VOLTS







**Quality Semi-Conductors** 

#### MCR63-1 thru MCR63-10 • MCR64-1 thru MCR64-10 • MCR65-1 thru MCR65-10

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case Pressfit and Stud Isolated Stud	R <sub>6</sub> JC	1	°C/W

(1) VRRM for all types can be applied on a continuous dc basis without incurring damage Ratings apply for zero or negative gate voltage. Devices shall not have a positive bias applied to the gate concurrently with a negative potential on the anode.

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic		Symbol	Min	Max	Unit
Peak Forward or Reverse Blocking Current (Rated VDRM or VRRM, gate open) TJ = TJ	25°C 125°C	<sup>I</sup> DRM <sup>, I</sup> RRM	=	10 2	μ AmA
Forward "On" Voltage (ITM = 175 A Peak)		∨тм	-	2	Volts
Gate Trigger Current (Continuous dc) ( $V_D = 12 V, R_L = 50 \Omega$ )	T <sub>C</sub> = 25°C T <sub>C</sub> = -40°C	İGT	-	40 75	mA
Gate Trigger Voltage (Continuous dc) $(V_D = 12 V, R_L = 50 \Omega)$ $(V_D = Rated V_{DRM}, R_L = 1 k\Omega, T_J = 125$	T <sub>C</sub> = 25℃ T <sub>C</sub> = -40℃ ℃)	VGT		3 3.5 —	Volts
Holding Current ( $V_D = 12 V, R_L = 50 \Omega, Gate Open$ )	<u></u>	и	-	60	mA
Forward Voltage Application Rate (TJ = 125°C, VD = Rated VDRM)		dv/dt	50	-	V/µs