

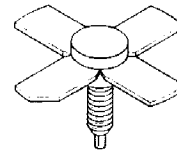
The RF Line NPN Silicon RF Power Transistor

Designed for 12.5 Volt UHF large-signal amplifier applications in industrial and commercial FM equipment operating to 512 MHz.

- Specified 12.5 Volt, 512 MHz Characteristics
 - Output Power = 10 W
 - Gain = 8.0 dB (Typ)
 - Efficiency = 65% (Typ)
- Gold Metallized, Emitter Ballasted for Long Life and Reliability
- Capable of 20:1 VSWR Load Mismatch at 16 V Supply Voltage

MRF653

10 W, 512 MHz
RF POWER
TRANSISTOR
NPN SILICON



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	16.5	Vdc
Collector-Base Voltage	V_{CBO}	38	Vdc
Emitter-Base Voltage	V_{EBO}	4.0	Vdc
Collector Current — Continuous	I_C	2.75	Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	44 0.25	Watts W/°C
Storage Temperature Range	T_{stg}	-65 to +150	°C
Operating Junction Temperature	T_J	200	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	4.0	C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage ($I_C = 20 \text{ mAdc}$, $I_B = 0$)	$V_{(BR)CEO}$	16.5	—	—	Vdc
Collector-Emitter Breakdown Voltage ($I_C = 20 \text{ mAdc}$, $V_{BE} = 0$)	$V_{(BR)CES}$	38	—	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 5.0 \text{ mAdc}$, $I_C = 0$)	$V_{(BR)EBO}$	4.0	—	—	Vdc
Collector Cutoff Current ($V_{CE} = 15 \text{ Vdc}$, $V_{BE} = 0$)	I_{CES}	—	—	5.0	mAdc

ON CHARACTERISTICS

DC Current Gain ($I_C = 1.0 \text{ Adc}$, $V_{CE} = 5.0 \text{ Vdc}$)	h_{FE}	20	—	120	—
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DYNAMIC CHARACTERISTICS

Output Capacitance ($V_{CB} = 12.5 \text{ Vdc}$, $I_E = 0$, $f = 1.0 \text{ MHz}$)	C_{ob}	—	22	28	pF
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FUNCTIONAL TESTS

Common-Emitter Amplifier Power Gain ($V_{CC} = 12.5 \text{ Vdc}$, $P_{out} = 10 \text{ W}$, $f = 512 \text{ MHz}$)	G_{pe}	7.0	8.0	—	dB
Collector Efficiency ($V_{CC} = 12.5 \text{ Vdc}$, $P_{out} = 10 \text{ W}$, $f = 512 \text{ MHz}$)	η_c	55	65	—	%
Load Mismatch Stress ($V_{CC} = 16 \text{ Vdc}$, $f = 512 \text{ MHz}$, $P_{in} (1) = 2.6 \text{ W}$, $VSWR = 20:1$, All Phase Angles)	ψ	No Degradation in Output Power			

NOTE:

- $P_{in} = 2.0 \text{ dB}$ over the typical input power required for 10 W output power @ 12.5 Vdc.

