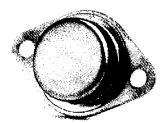
20 STERN AVE. SPRINGFIELD, NEW JERSEY 07081 U.S.A. TELEPHONE: (973) 376-2922

(212) 227-6005

FAX: (973) 376-8960

# **PMD 20K SERIES**

## 150 WATT (14 AMP CONTINUOUS, 20 AMP PEAK)



#### **FEATURES**

- Electrical specifications guaranteed for operating junction temperature range of 0 - 150°C
- Guaranteed and 100% tested for I<sub>SB</sub> (Secondary Breakdown Current) insuring maximum performance at high energy levels
- Built-in speed up diode for fast turn-off with negative base drive
- Low thermal resistance for more useable power and lower operating temperatures
- Hermetically sealed

#### DESCRIPTION

The PMD 20K Series of devices are three-terminal NPN Switching Darlington Power Transistors. These devices are monolithic epitaxial base structures with built-in base to emitter shunt resistors. They have been designed to switch at frequencies up to 30kHz. The devices are CVD glass passivated to increase reliability and provide reduced hightemperature reverse leakage current. Internal diode protection (D1) of the Darlington configuration is built into the structure to limit the device power dissipation during negative overshoot. Diode D2 is built-in to reduce device turn-off time when negative base drive is used.

#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MAXIMUM	UNITS
Collector Emitter Voltage PMD 20K120 PMD 20K150 PMD 20K200	V <sub>CEO</sub>	120 150 200	Vdc
Collector Base Voltage PMD 20K120 PMD 20K150 PMD 20K200	V <sub>CBO</sub>	120 150 200	Vdç
Emitter Base Voltage	V <sub>EBO</sub>	2	Vdc
Collector Current Continuous Peak	I <sub>C</sub>	14 20	Adc
Base Current	I <sub>B</sub>	0.5	Adc
Thermal Resistance	θ <sub>JC</sub>	1.0	°C/Watt
Total Internal Power Dissipation @ T <sub>C</sub> = 0°C1	P <sub>D</sub>	150	Watts
Operating Junction and Storage Temperature	T <sub>J</sub> T <sub>STG</sub>	-65 to +150 -65 to +200	°C

 $<sup>^{(</sup>i)}$  For operation above  $T_{c} = 0^{\circ}C$ , derate  $@ 1 \text{ W/}^{\circ}C$ .

## **DEVICE SELECTION GUIDE**

DEVICE	VOLTAGE RATING	
PMD 20K120	120V	
PMD 20K150	150V	
PMD 20K200	200V	

Excellent thermal resistance junction to case ( $\theta_{JC}$ ) provides for more useable power at lower operating temperatures. This, coupled with 100%  $I_{SB}$  testing, insures optimum performance and durability in power applications such as switching regulators and inverters. These Darlington devices are hermetically sealed steel TO-3 packages providing high reliability and low thermal resistance.

NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

**Quality Semi-Conductors** 

# **PMD 20K SERIES**

### **ELECTRICAL CHARACTERISTICS**

All parameters are guaranteed at  $T_{\rm J}=0$  to 150°C, unless otherwise specified.

Parameter	Symbol	Test Conditions	Minimum	Maximum	Units
ON CHARACTERISTICS					
Collector Emitter Saturation Voltage <sup>1</sup>	V <sub>CE(sat)</sub>	$I_C = 10 \text{ Adc}$ ; $I_8 = 50 \text{ mAdc}$		1.7 <sup>2</sup> 1.8	Vdc
Base Emitter Turn-on Voltage¹	V <sub>BE(on)</sub>	I <sub>C</sub> = 10 Adc; V <sub>CE</sub> = 3 Vdc		2.6 <sup>2</sup> 2.8	Vdc
Base Emitter Saturation <sup>1</sup>	V <sub>BE(sat)</sub>	$I_C = 10 \text{ Adc}; I_B = 50 \text{ mAdc}$		2.6 <sup>2</sup> 2.8	Vdc
DC Current Gain <sup>1</sup>	h <sub>FE</sub>	I <sub>C</sub> = 10 Adc; V <sub>CE</sub> = 3 Vdc	300		
Forward Bias Secondary Breakdown Current	l <sub>s/b</sub>	V <sub>CE</sub> = 26 Vdc; T <sub>A</sub> = 25°C 1 sec non-repetitive pulse	5.8		Adc
Secondary Breakdown Energy	E <sub>s/b</sub>	$I_C = 8.43 \text{ Adc}; L = 45 \mu H$ $T_A = 25 ^{\circ} C$	1.6		mJoules
OFF CHARACTERISTICS					
Collector Emitter Breakdown Voltage <sup>1</sup> (Base Open)	V <sub>(BR)CEO</sub>	$I_{CE} = 100 \text{ mAdc}; I_B = 0$ $T_J = 25^{\circ}C$			Vdc
PMD 20K120 PMD 20K150 PMD 20K200			120 150 200		
Collector Emitter Sustaining Voltage <sup>1</sup> PMD 20K120 PMD 20K150 PMD 20K200	V <sub>(SUS)</sub> CER	$I_{CE} = 100 \text{ mAdc}; R_{BE} = 2.2 \text{k}\Omega$	120 150 200		Vdc
Emitter Base Leakage Current	I <sub>EBO</sub>	$V_{EB} = 0.9 \text{ Vdc}; I_C = 0A$		70	mAdc
Collector Emitter Leakage Current PMD 20K120 PMD 20K150 PMD 20K200	I <sub>CER</sub>	$V_{CE} = 80 \text{ Vdc}; R_{BE} = 2.2 \text{k}\Omega$ $V_{CE} = 100 \text{ Vdc}; R_{BE} = 2.2 \text{k}\Omega$ $V_{CE} = 150 \text{ Vdc}; R_{BE} = 2.2 \text{k}\Omega$		5.0 5.0 5.0	mAdc
Collector Emitter Leakage Cürrent (Base Open) <sup>2</sup> PMD 20K120 PMD 20K200	I <sub>CEO</sub>	V <sub>CE</sub> = 80 Vdc V <sub>CE</sub> = 100 Vdc V <sub>CE</sub> = 150 Vdc		0.5 0.5 0.5	mAdc
DYNAMIC CHARACTERISTIC	§2,3			<u> </u>	
Rise Time	t,	I <sub>C</sub> = 10 Adc; V <sub>CC</sub> = 30V		0.3	μS
Turn-On Time	t <sub>on</sub>	I <sub>C</sub> = 10 Adc; V <sub>CC</sub> = 30V		0.5	μS
Fall Time	t <sub>f</sub>	I <sub>C</sub> = 10 Adc; V <sub>CC</sub> = 30V		0.8	μS
Turn-Off Time	t <sub>off</sub>	I <sub>C</sub> = 10 Adc; V <sub>CC</sub> = 30V		1.0	μS

<sup>(1)</sup> Pulse tested with pulse width  $\pm$  300  $\mu S$  and duty cycle  $\leq$  2.0%. 
(2) T, = 25°C. 
(3) See AC test circuit.